

FIG. 1

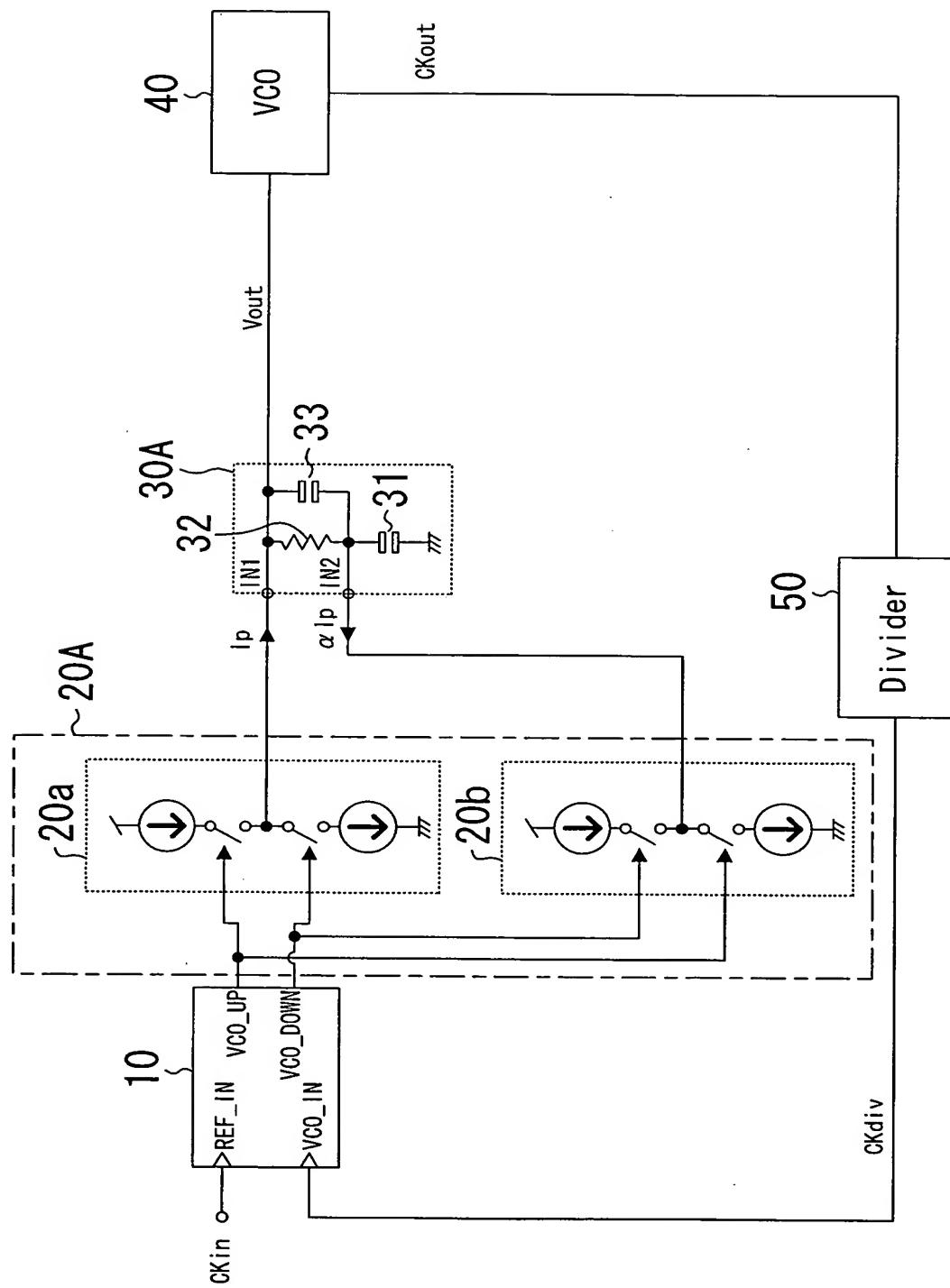
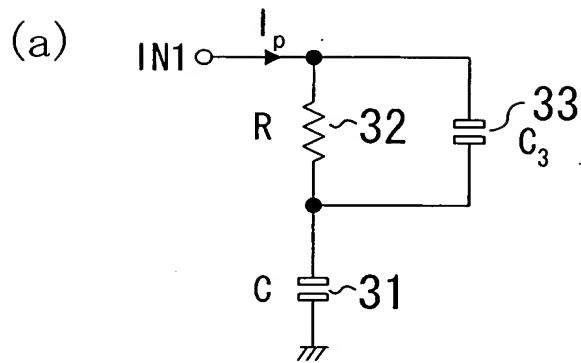
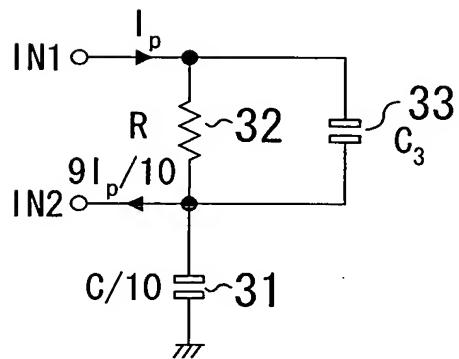


FIG. 2

(a) 30

↑ Equivalent  
conversion

(b) 30A

↑ Equivalent  
conversion

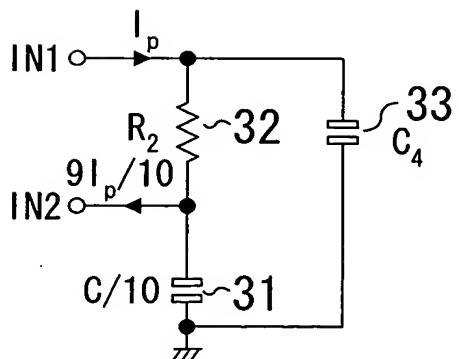
(c) 30B

FIG. 3

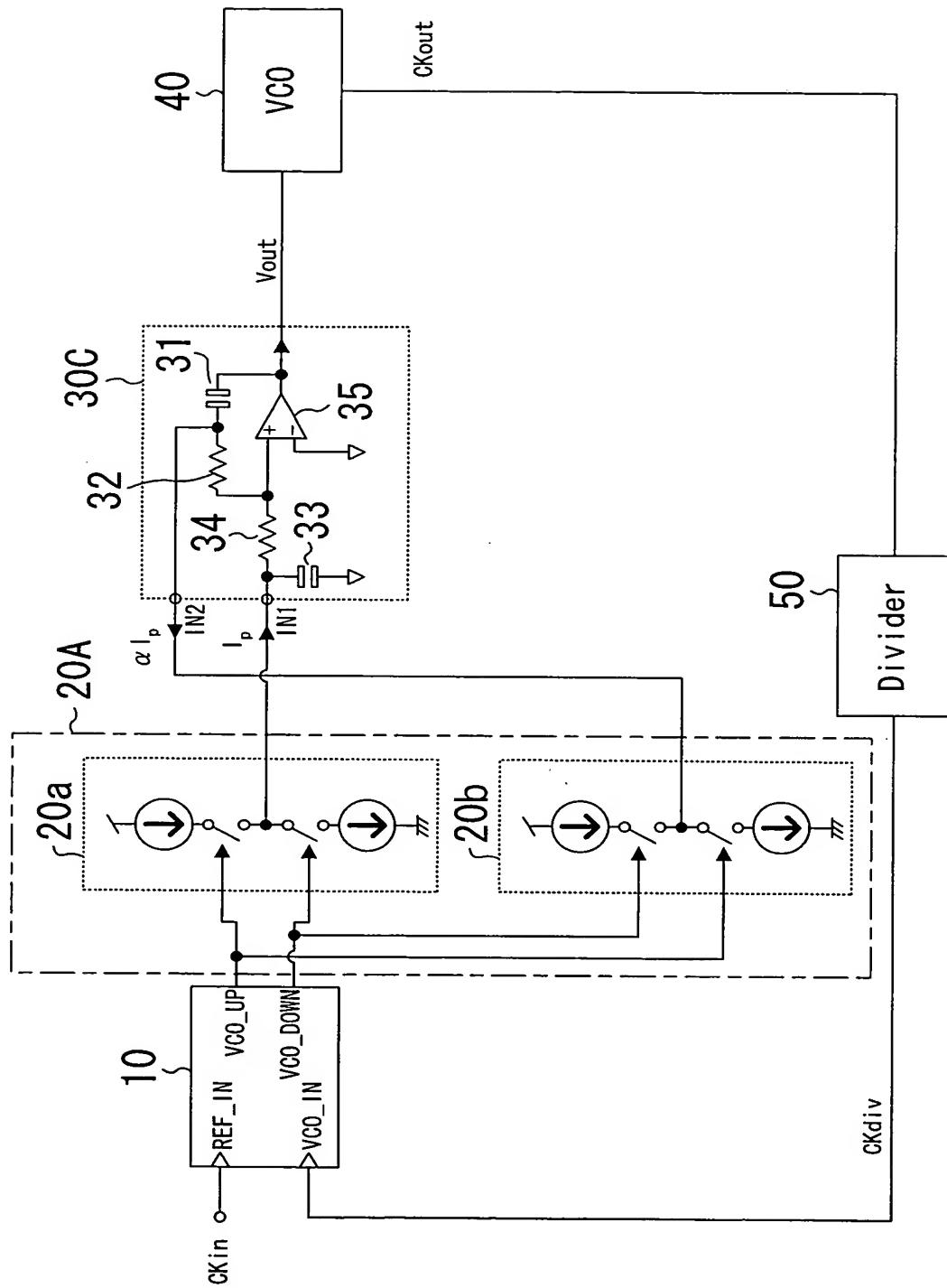
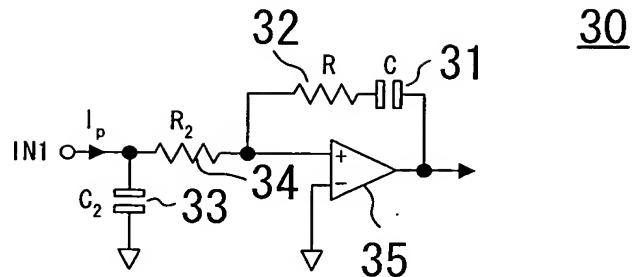


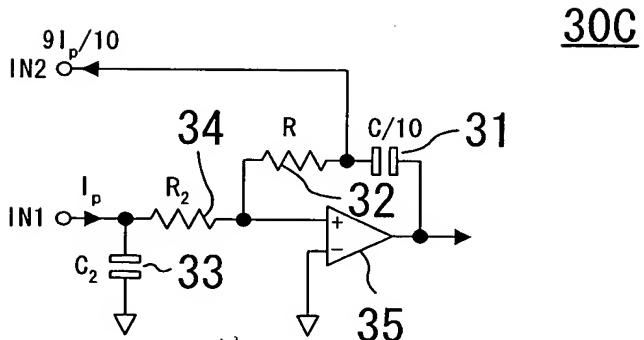
FIG. 4

(a)



↔ Equivalent conversion

(b)



↔ Equivalent conversion

(c)

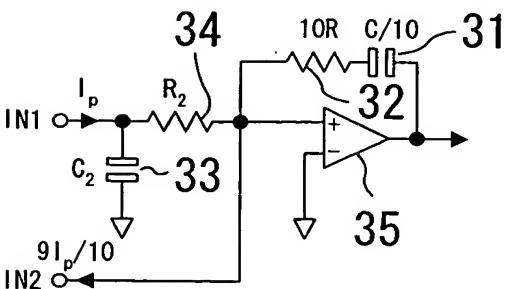


FIG. 5

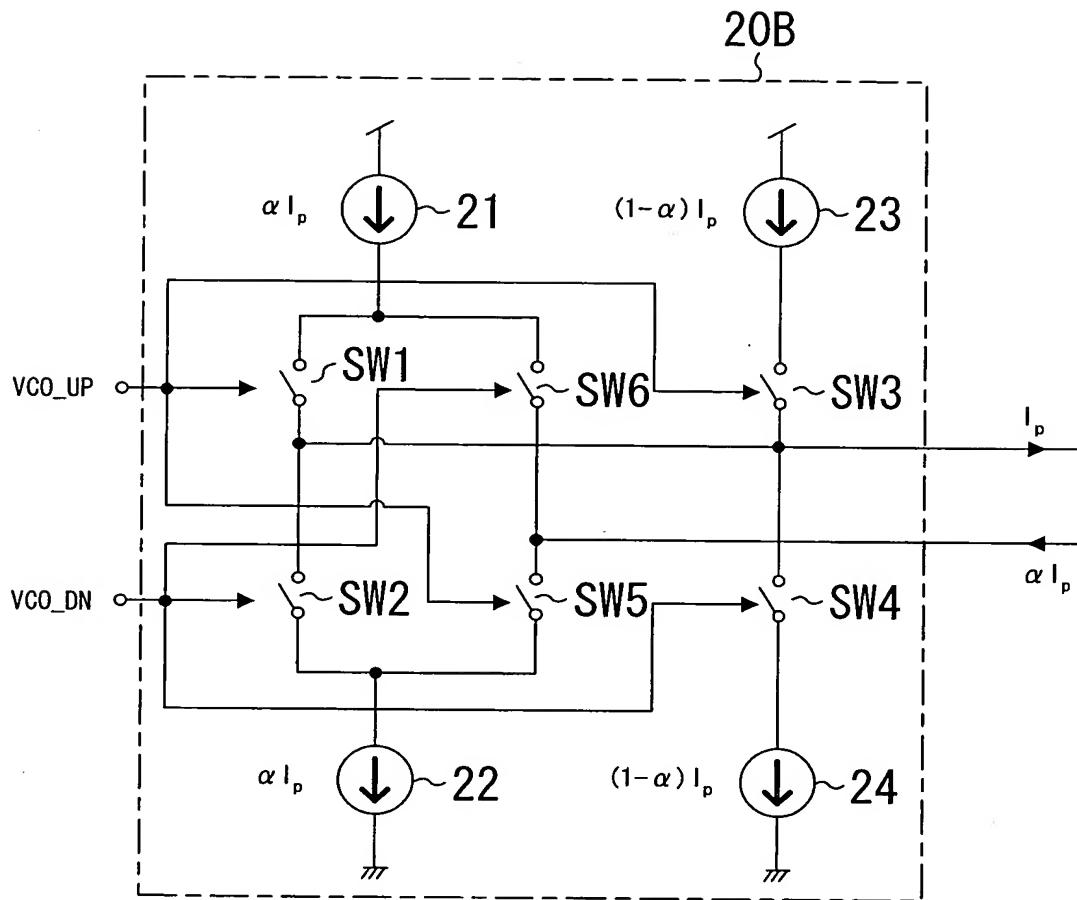


FIG. 6

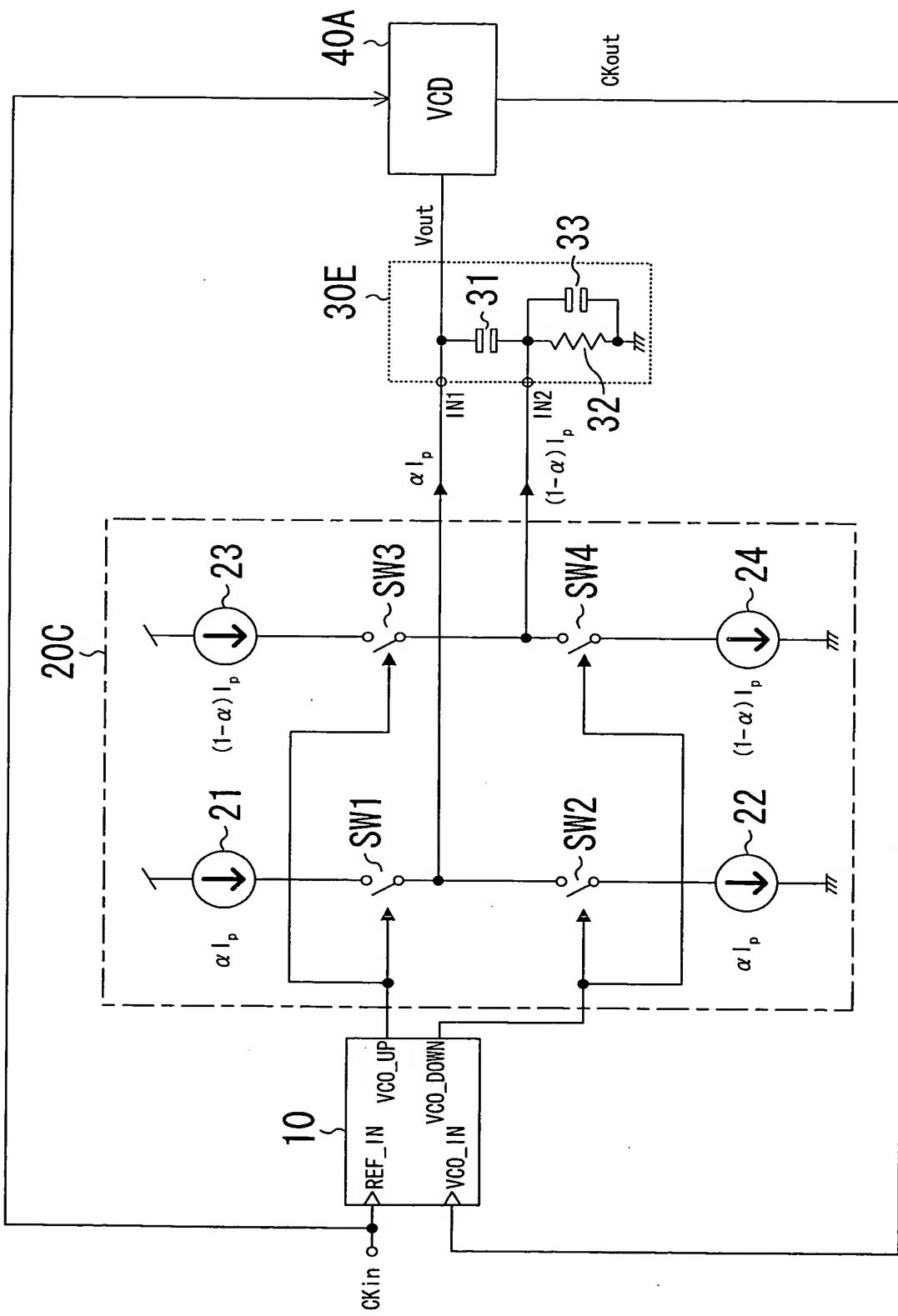
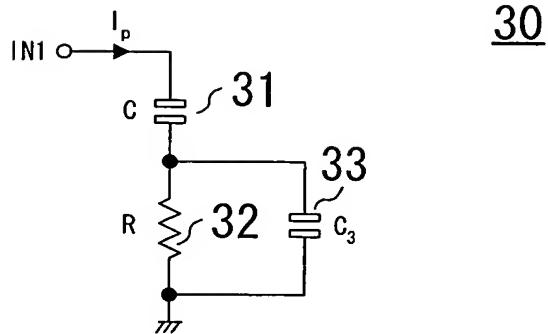


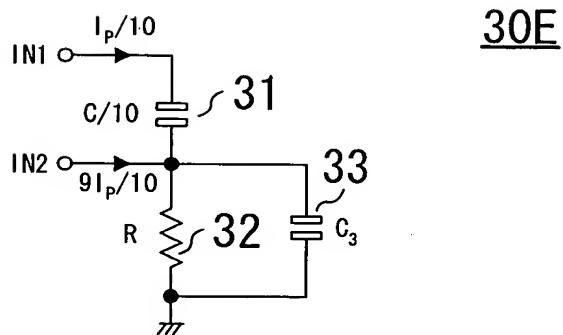
FIG. 7

(a)



Equivalent  
conversion

(b)



Equivalent  
conversion

(c)

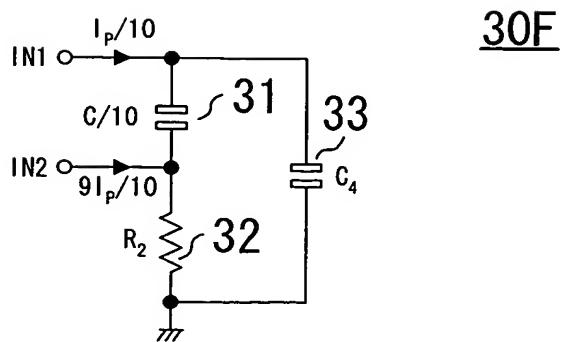


FIG. 8

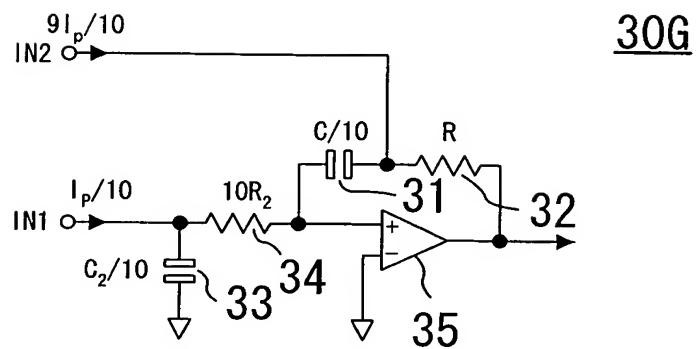
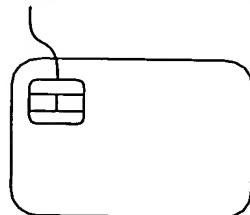


FIG. 9

LSI device incorporating PLL or  
DLL of the present invention

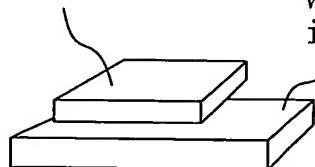


Application to IC card

FIG. 10

COC component  
incorporating PLL or DLL  
of the present invention

Base material to  
which COC component  
is attached



Application to COC component

FIG. 11

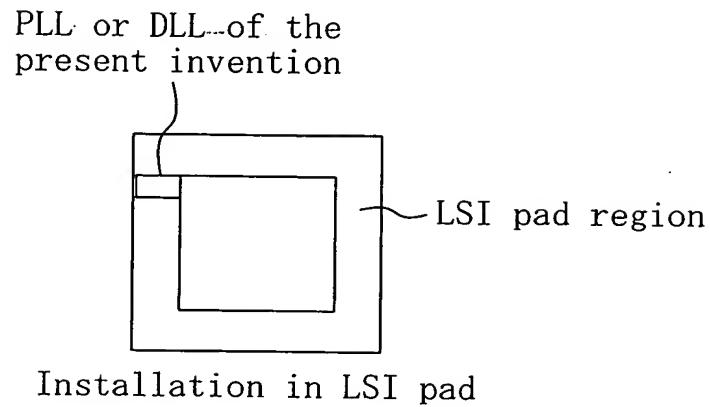


FIG. 12

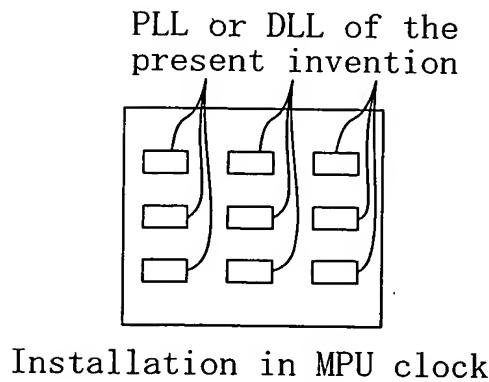


FIG. 13

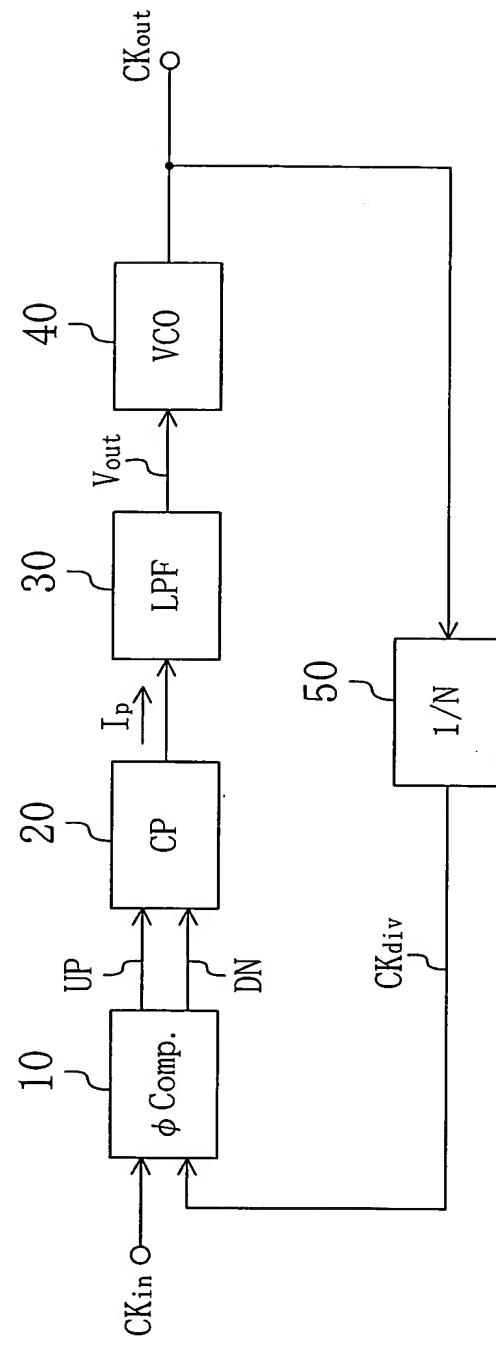


FIG. 14A

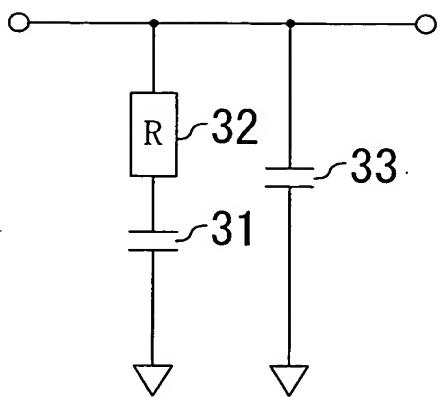
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FIG. 14B

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